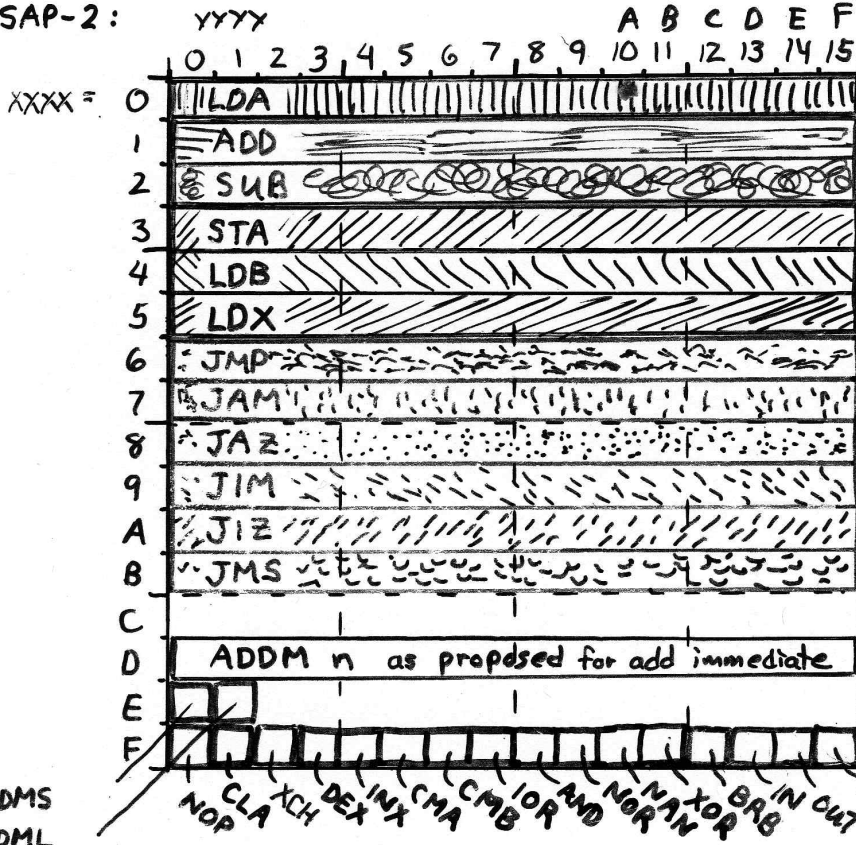


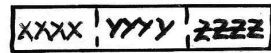
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A Look at Instruction Space

SAP-2:



12 bit instructions

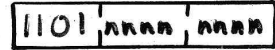


M.S. 4 bits
Middle 4 bits
4 bits not available to control

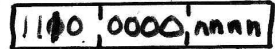
8 bits can be used for opcodes.
= 256 combinations

Possible new instructions:

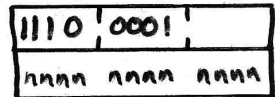
ADDM n (8 bits)



ADDMS n (4 bits)

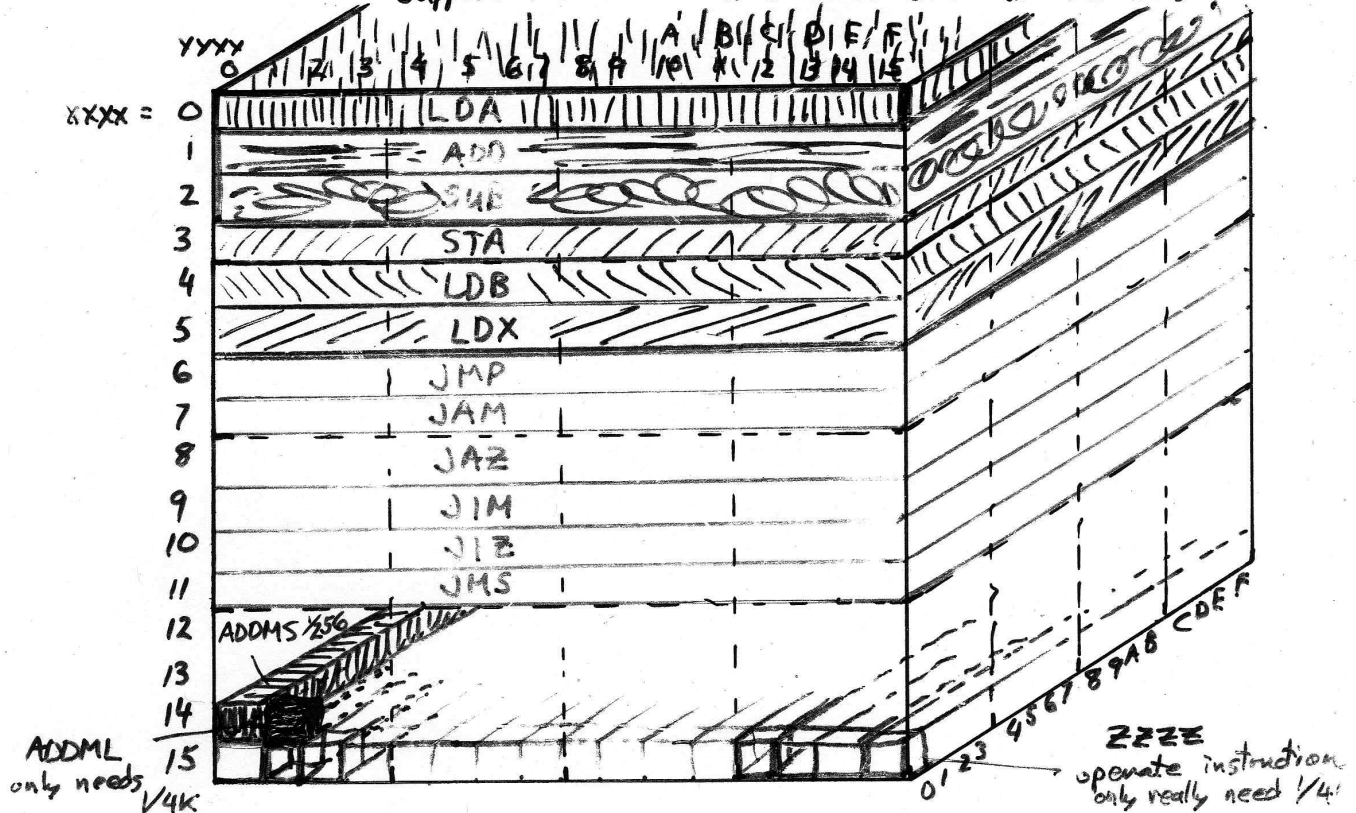


ADDML n (12 bits)



ADDMS
ADDML

Suppose the control unit could see all 12 bits:



ADDML only needs 1/4K

ZZZZ operate instruction only, really need 1/4K